



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Stubbs et al.

Serial No.: 10/624,772

Filed: July 22, 2003

For: ACTIVELY DRIVEN V_{REF} FOR
INPUT BUFFER NOISE IMMUNITY

Confirmation No.: 6777

Examiner: C. Yoha

Group Art Unit: 2818

Attorney Docket No.: 2269-4364.1US
(00-0252.01/US)

Notice of Allowance Mailed:

January 13, 2005

NOTICE OF EXPRESS MAILING

Express Mail Mailing Label Number: EL994844475US

Date of Deposit with USPS: April 13, 2005

Person making Deposit: Steve Wong

TRANSMITTAL LETTER

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:


Applicants submit herewith Part B - Fee(s) Transmittal for the above-captioned application and a check in the amount of \$1,715.00 in payment therefor plus five (5) copies of the patent when issued.

Also, enclosed are Comments on Statement of Reasons for Allowance (2 pages); and Fee Addressee for Receipt of PTO Notices Relating to Maintenance Fees (2 pages).

Serial No.: 10/624,772

Applicants understand that no additional fees are required. However, if the Office determines that any comparison fees or other additional fees are required, the Commissioner is authorized to charge any such fees to TraskBritt Deposit Account No. 20-1469. A copy of this Transmittal Letter is enclosed for deposit account charging purposes.

Respectfully submitted,



Kevin K. Johanson
Registration No. 38,506
Attorney for Applicant(s)
TRASKBRITT
P.O. Box 2550
Salt Lake City, Utah 84110-2550
Telephone: 801-532-1922

Date: April 13, 2005
KKJ/ps:lmh

Enclosures: Part B - Issue Fee Transmittal
Check No. 21560 in the amount of \$1,715.00
Copy of Transmittal Letter
Comments on Statement of Reasons for Allowance (2 pages)
Fee Addressee for Receipt of PTO Notices Relating to Maintenance Fees (2 pages)

Document in ProLaw



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COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The Examiner indicates:

Claims 1-8 are considered allowable since prior art made of record and considered pertinent to the applicant's disclosure does not teach or suggest the claimed limitations having in combination with other features, a circuit configured to receive an external reference voltage as generated external to the memory device and generate in response thereto an internal reference voltage independent

of the power supply voltage, the internal reference voltage for accessing and evaluating a logic state of the at least one data bit in the memory array.

Applicants concur with the reasons as stated by the Examiner insofar as they comprise a summary, and are exemplary and not limiting. However, the independent claims as allowed include other and different language than that specified by the Examiner, and the allowed dependent claims include other and further features and elements. Accordingly, the scope of the claims must be determined from the literal language of each as a whole, as well as equivalents thereof.

Respectfully submitted,



Kevin K. Johanson
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P.O. Box 2550
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Date: April 13, 2005

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